

FORM HDP-1449 (Based on Form PTO-1449)		ATTORNEY DOCKET NO.	SERIAL NO.
		MP0074.C1	10/601,286
		APPLICANT	
		Farbod ARAM et al.	
		FILING DATE	GROUP
		June 20, 2003	UNKNOWN

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U.S. PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date (mm/dd/yyyy)	Name	Class/ Subclass	(If appropriate) Filing Date
	NH	6,597,562	07/22/2003	Hu et al.		

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Ref. Desig.	Examiner's Initials	Document Number	Date (mm/dd/yyyy)	Country	Class/ Subclass	Translation Yes No
	NH	JP 2-178963	07/11/1990	JAPAN		X (Abstract)
		JP 64-84616	03/29/1989	JAPAN		X
		JP 59-105341	06/18/1984	JAPAN		X (Abstract)
		JP 01096943	04/14/1989	JAPAN		X (Abstract)
		JP 02086160	03/27/1990	JAPAN		X (Abstract)
		JP 02090561	03/30/1990	JAPAN		X (Abstract)
		JP 02178963	07/11/1990	JAPAN		X (Abstract)
		JP 02191369	07/27/1990	JAPAN		X (Abstract)
		JP 03096267	04/22/1991	JAPAN		X (Abstract)
		JP 03133169	06/06/1991	JAPAN		X (Abstract)
		JP 03181165	08/07/1991	JAPAN		X (Abstract)
		JP 61055936	03/20/1986	JAPAN		X (Abstract)
		JP 61263251	11/21/1986	JAPAN		X (Abstract)
		JP 54-101153	08/09/1979	JAPAN		X
		JP 59-197122	11/08/1984	JAPAN		X
	↓ NH	JP 60-1825	01/08/1985	JAPAN		X
	NH	JP 60-4207	01/10/1985	JAPAN		X

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Ref. Desig.	Examiner's Initials	
		Shiba, Takashi et al. "Study on Transformer Ratio and Capacitance in Equivalent Circuit Model for SAW Transducer." 10 th Symposium on Ultrasonic Electronics (Tokyo 1989) <u>Japanese Journal of Applied Physics</u> , Vol. 29 (1990) Supplement 29-1, pp. 129-132.
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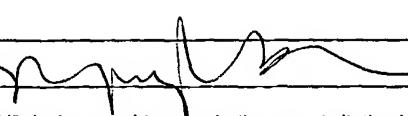
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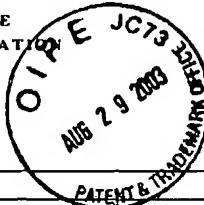
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	WY	JP 1-120858	05/12/1989	JAPAN	—	X (Abstract)
	NH	JP 3-257855	11/18/1991	JAPAN	—	X (Abstract)

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	WA	5,208,725	05/04/1993	Akcasu	—	

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1.	NH	5,583,359	12-1996	Ng et al.	/	
2.		5,903,493	05-1999	Lee	/	
3.		6,001,702	12-1999	Cook et al.	/	
4.		6,150,707	11-2000	Cook et al.	/	
5.		6,266,226	07-2001	Hayashi	/	
6.		6,266,227	07-2001	Konushi et al.	/	
7.		6,312,963	11-2001	Chou et al.	/	
8.		6,414,806	07-2002	Gowda et al.	/	
9.		6,445,056	09-2002	Nakashima	/	
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11.	NH	6,501,610	12-2002	Sugawara et al.	/	

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Ref. Desig.	Examiner's Initials
1.	NH

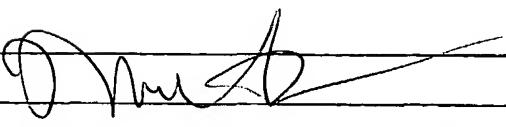
Bhupendra K. Ahuja, "An Improved Frequency Compensation Technique for CMOS Operational Amplifiers", IEEE J. Solid-State Circuits, vol. SC 18, no. 6, pp. 629-633, Dec. 1983.

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2.	WHT	Runsheng He, Nersi Nazari and Sehat Sutardja, "A DSP Receiver for 1000BASE-T PHY," Submitted to ISSCC 2001. <i>Date / Month / Year Missing</i>
3.	↓	S. Sutardja and P.R. Gray, "A pipelined 13-bit, 250-ks/s, 5-V, analog-to-digital converter," IEEE J. Solid-State Circuits, vol. 23, pp. 1316-1323, Dec. 1988.
4.	↓	S.H. Lewis and P.R. Gray, "A pipelined 5-Msample/s 9-bit analog to digital converter," IEEE J. Solid-State Circuits, vol. SC-22, pp. 954-961, Dec. 1987.
5.	PWT	S.H. Lewis, R. Ramachandran "A 10-b 20-Msample/s Analog-to-Digital Converter," IEEE J. Solid-State Circuits, vol. 27, pp. 351-358, March 1992.

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